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Electronic Version v1.1

Stylesheet Version v1.1.0

| Title of |
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| Invention |

Memory Module with Dynamic Termination Using Bus Switches Timed by Memory Clock and Chip Select

Application Number:

Date:

First Named Applicant:

Yao Tung Yen

Confirmation Number:

Attorney Docket Number:

PS-122

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| Submitted By: | Elec. Sign. | Sign. Capacity |
|---------------------------|---------------|----------------|
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Documents being submitted: Files us-fee-sheet PS122ex-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd PS122ex-usidst.xml us-ids us-ids.dtd us-ids.xsl PS122ex-usrequ.xml us-request us-request.dtd us-request.xsl us-declaration OathPS122e.tif PS122e-trans.xml application-body us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogrk3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd figPS122e01.TIF figPS122e02.TIF figPS122e03.TIF figPS122e04.TIF figPS122e05.TIF figPS122e06.TIF figPS122e07.TIF Comments